

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1017413	processor microprocessor mpu cpu	US-PGPUB; USPAT	OR	ON	2008/02/05 16:03
L2	223988	(reduc\$3 conserv\$3 manage\$4) near3 power	US-PGPUB; USPAT	OR	ON	2008/02/05 16:04
L3	91672	1 and 2	US-PGPUB; USPAT	OR	ON	2008/02/05 16:05
L4	7125	state adj register	US-PGPUB; USPAT	OR	ON	2008/02/05 16:06
L5	25124	control adj register	US-PGPUB; USPAT	OR	ON	2008/02/05 16:06
L6	5366	3 and (4 or 5)	US-PGPUB; USPAT	OR	ON	2008/02/05 16:06
L7	95317	clock same voltage	US-PGPUB; USPAT	OR	ON	2008/02/05 16:07
L8	1926	6 and 7	US-PGPUB; USPAT	OR	ON	2008/02/05 16:07
L9	187935	(status state mode) near5 power	US-PGPUB; USPAT	OR	ON	2008/02/05 16:09
L10	8999	(status state mode) near5 (power adj3 signal)	US-PGPUB; USPAT	OR	ON	2008/02/05 16:10
L11	2558	10 same voltage	US-PGPUB; USPAT	OR	ON	2008/02/05 16:11
L12	1139	10 same clock	US-PGPUB; USPAT	OR	ON	2008/02/05 16:11
L13	131	8 and 11	US-PGPUB; USPAT	OR	ON	2008/02/05 16:12
L14	118	8 and 12	US-PGPUB; USPAT	OR	ON	2008/02/05 16:12
L15	248833	\$3unit near3 (processor microprocessor cpu mpu)	US-PGPUB; USPAT	OR	ON	2008/02/05 16:14
L16	110	13 and 15	US-PGPUB; USPAT	OR	ON	2008/02/05 16:15
L17	67	13 and 14	US-PGPUB; USPAT	OR	ON	2008/02/05 16:15
L18	12	17 not ink	US-PGPUB; USPAT	OR	ON	2008/02/05 16:20
L19	80	14 and 15	US-PGPUB; USPAT	OR	ON	2008/02/05 17:14
L20	25	19 not ink	US-PGPUB; USPAT	OR	ON	2008/02/05 17:15
L21	18	20 not 18	US-PGPUB; USPAT	OR	ON	2008/02/05 17:15

## EAST Search History

L22	15	16 not ink	US-PGPUB; USPAT	OR	ON	2008/02/05 17:49
L23	8	22 not (21 or 18)	US-PGPUB; USPAT	OR	ON	2008/02/05 17:50